

Datasheet

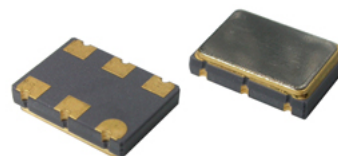
SX7CF

HCMOS SURFACE MOUNT CRYSTAL CLOCK OSCILLATOR

FEATURES

- SMD package
- Phase jitter less then 0.5 ps
- Applications: Optical, SONET, xDSL, SDH,....

7.0 x 5.0 x 1.8 mm



Item	Specification	
Frequency Range	125.01 MHz-320.0 MHz	
Output Logic	CMOS	
Overall Frequency Stability*	± 25 ppm ~ ± 100 ppm (see options)	
Operating Temperature Range	0~ +70°C commercial application (see options) -40~ +85°C industrial application (see options)	
Supply Voltage Vdd	+2.5V	+3.3V
Supply Current Idd	45mA max	
Output Level	VOH ≥ 0.9 Vdd	VOL ≤ 0.1 Vdd
Output Load	15pF	
Symmetry	45/55%	
Rise Time / Fall Time Fr/Ff	0.7 ns typ.	
Tri-state function (see options)	pin#1 or #2 = high or open pin#1 or #2 = low	pin#4 ==> oscillation pin#4 ==> high impedance
Start-up Time	10 ms max.	
Integrated Phase Jitter(12kHz to 20MHz)	0.4 ps typical; 0.5 ps max (For 156.250 MHz)	
Period Jitter RMS	30 ps typical; 5.0 ps max (For 156.250 MHz)	
Period Jitter peak-to-peak	20.0 ps typical; 3.0 psmax (For 156.250 MHz)	
Phase Noise (typical)	Offset	Frequency 156.250 MHz
	10 Hz	-62 dBc / Hz
	100 Hz	-92 dBc / Hz
	1 kHz	-120 dBc / Hz
	10 kHz	-132 dBc / Hz
	100 kHz	-128 dBc / Hz
Packing Unit	1000pcs / reel	
Soldering Condition	260°C , 10 sec x2 max	
	Customer specification on request	

(*) Included initial tolerance @+25°C, stability over operating temperature, stability vs.load change, stability vs. supply change and one year aging

OPTIONS & ORDERING INFORMATION

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Supply Voltage	Operating Temp. *	Overall Stability *	Tri-state Function	Output Load *	Frequency in MHz
25 = +2.5V	E = 0° / +70°C	25 = ±25 ppm	E1 = Tri-state, pad #1	Blanc = 15pF	Please specify the frequency in MHz
33 = +3.3V	F = -20° / +70°C	30 = ±30 ppm	E2 = Tri-state, pad #2		
	K = -40° / +85°C	50 = ±50 ppm	F = no Tri-state		
		100 = ±100 ppm			

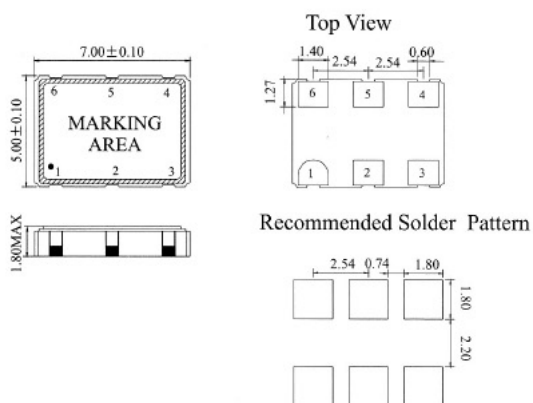
* Note : Not all combinations are possible, please consult us.

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OUTLINE DIMENSIONS



Pin Connections	#1 : E/D or NC	#2 : E/D or NC	#3: GND
	#4 : Output	#5 : NC	#6 : Vdd