

## Datasheet

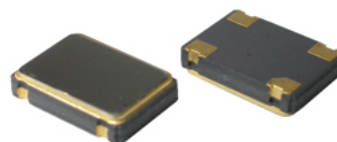
### SX5SRV

### TRUE SINE WAVE SURFACE MOUNT VCXO

## FEATURES

- Standard miniature package
- High purity and low total harmonic distortion
- Wide pulling range
- Applications: Audio modulation

5.0 x 3.2 x 1.2 mm



Item	Specification	
Frequency Range	10.0 MHz ~ 30.0 MHz	
Output Logic	True Sine Wave	
Overall Frequency Stability *	± 20 ppm ~ ± 100 ppm (see options)	
Operating Temperature Range	0 ~ +70 °C commercial application (see options) -40 ~ +85 °C industrial application (see options)	
Supply Voltage Vdd	+3.3 V ±5%	+5.0 V ±5%
Control Voltage Center	+1.65 V	+2.5V
Control Voltage Range	0.3V to 3.0V	0.5V to 4.5V
Supply Current Idd	1.1 mA	1.2 mA
Output Level	1.0 V p-p typical	
Output Load	10 kOhm // 10 pF	
Harmonics	<-25 dBc (frequency dependent)	
Sub-Harmonics	None	
Tri-state Function	No Tri-state option	
Start-up Time	2 ms typ.	
Frequency Pulling Range	standard ±50 ppm min. ; ±100 ppm min. ; ±150 ppm min. (see options)	
Linearity	6% typical; 10% max.	
Slope Polarity	Positive (Increasing control voltage always increases output frequency)	
Modulation Bandwidth	25 kHz min (-3 dB)	
Input Impedance	0.5 MΩ min.	
Packing Unit	1000 pcs / reel	
Soldering Condition	260 °C, 10 sec x2 max	

**Customer specifications on request**

(\*) Includes initial tolerance @+25°C, stability over operating temperature, stability vs. load change, stability vs. supply change and one year aging

## OPTIONS & ORDERING INFORMATION

### SX5SRV

.....	.....	.....	.....	.....	.....	..... MHz
Supply Voltage	Operating Temp. *	Overall Stability *	Tri-state Function	Package type	Pulling *	Frequency in MHz
<b>33</b> = +3.3 V	<b>E</b> = 0° / +70 °C	<b>20</b> = ±20 ppm	<b>F</b> = No Tri-state	<b>4P</b> = 4-pad	<b>50</b> = ±50 ppm min.	Please specify the
<b>50</b> = +5.0V	<b>F</b> = -20° / +70 °C	<b>25</b> = ±25 ppm		version	<b>100</b> = ±100 ppm min.	frequency in MHz
	<b>K</b> = -40° / +85 °C	<b>30</b> = ±30 ppm			<b>150</b> = ±150 ppm min.	
		<b>50</b> = ±50 ppm				
		<b>100</b> = ±100 ppm				

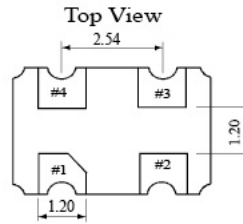
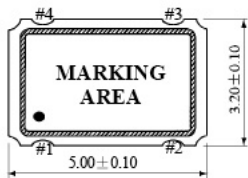
(\*) Note : Not all combinations are possible, please consult us.

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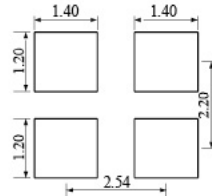
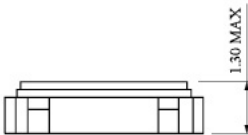
**SX5SRV**

**TRUE SINE WAVE SURFACE MOUNT VCXO**

**OUTLINE DIMENSIONS**



Recommended Solder Pattern



**Pin Connections**

#1 : Control Voltage

#2 : GND

#3: Output

#4 : Vdd